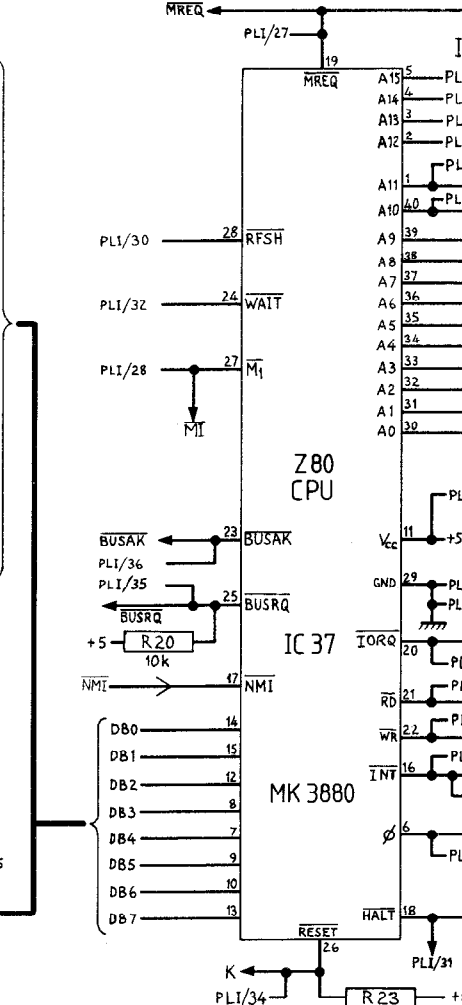
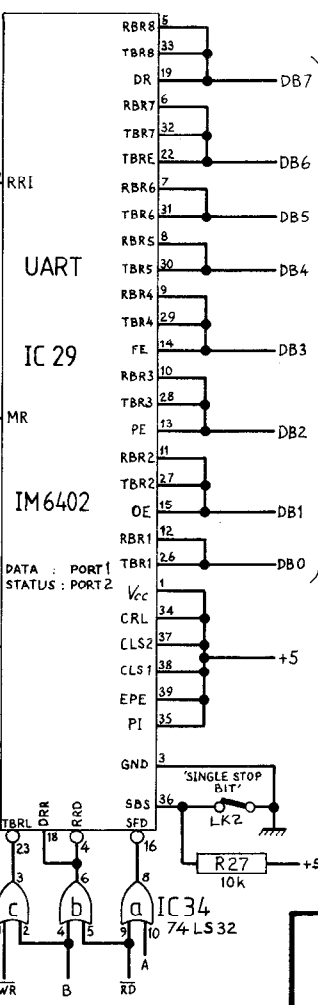
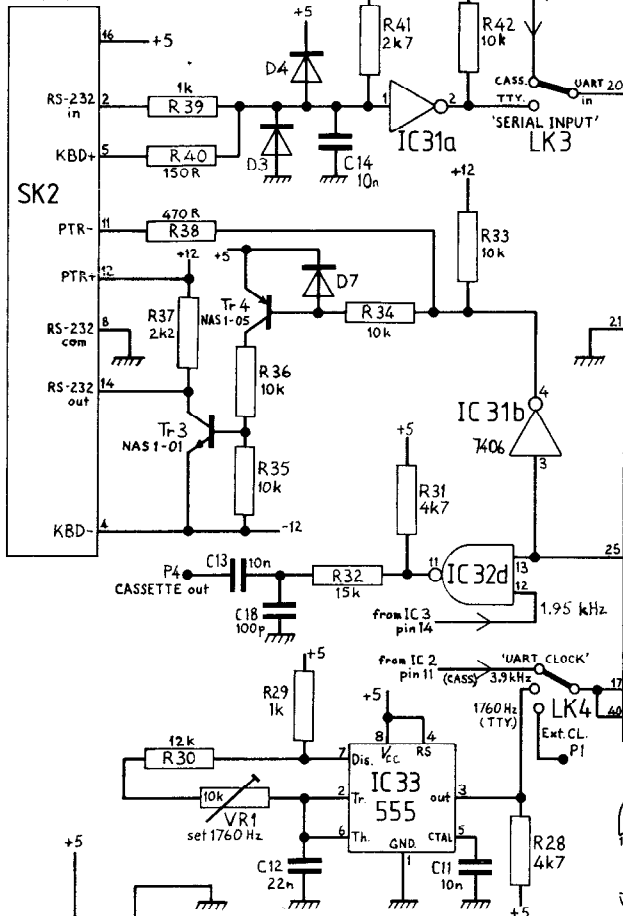
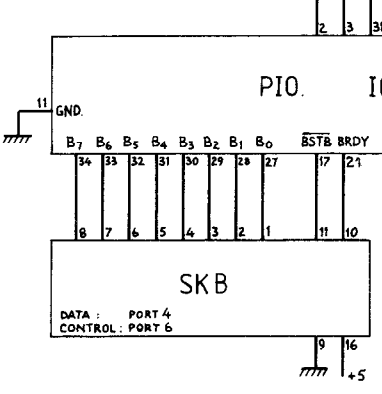
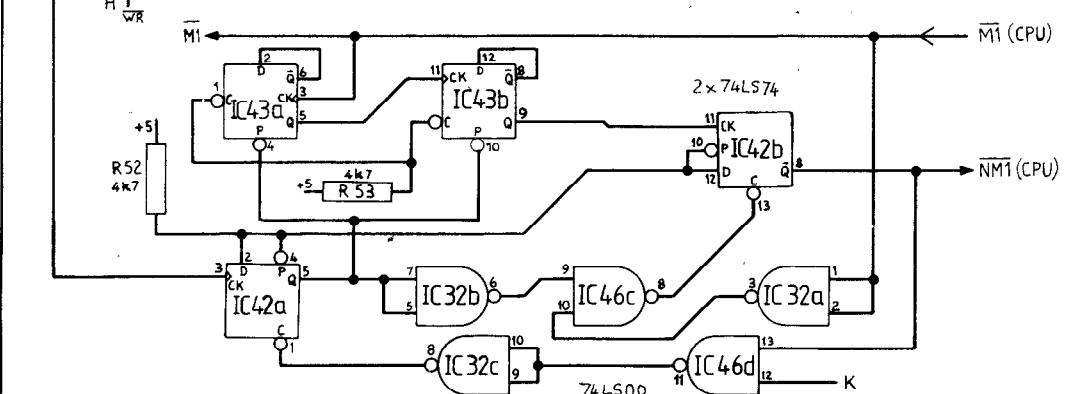
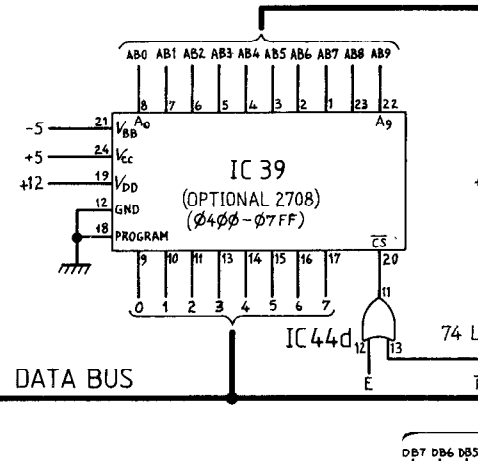
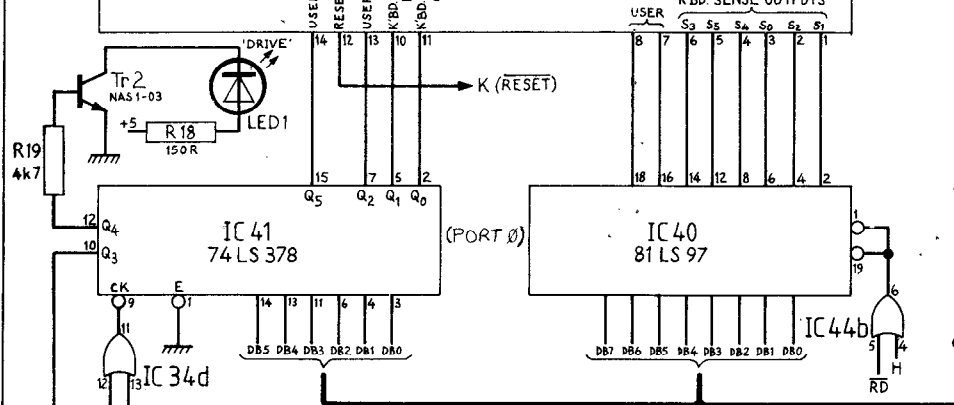


**SERIAL DATA SOCKET**



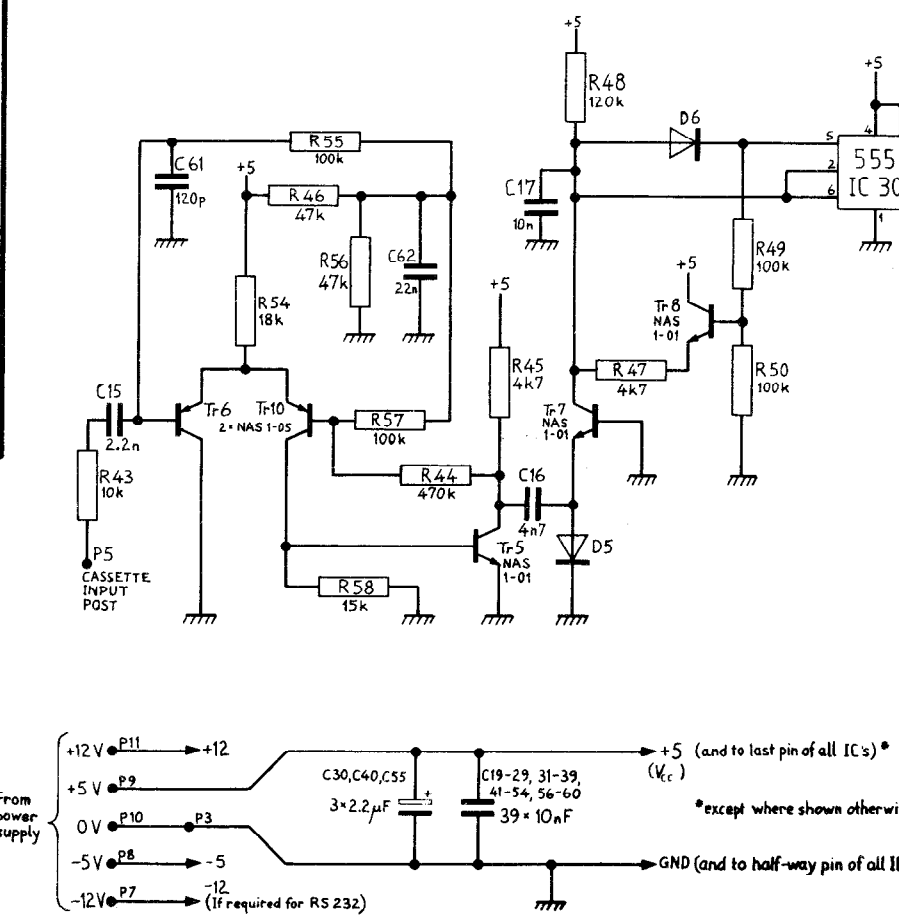
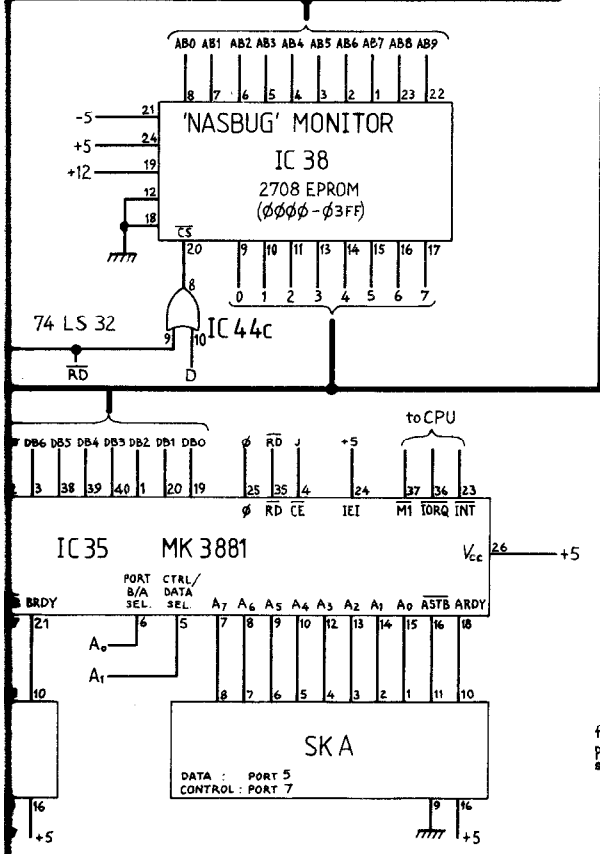
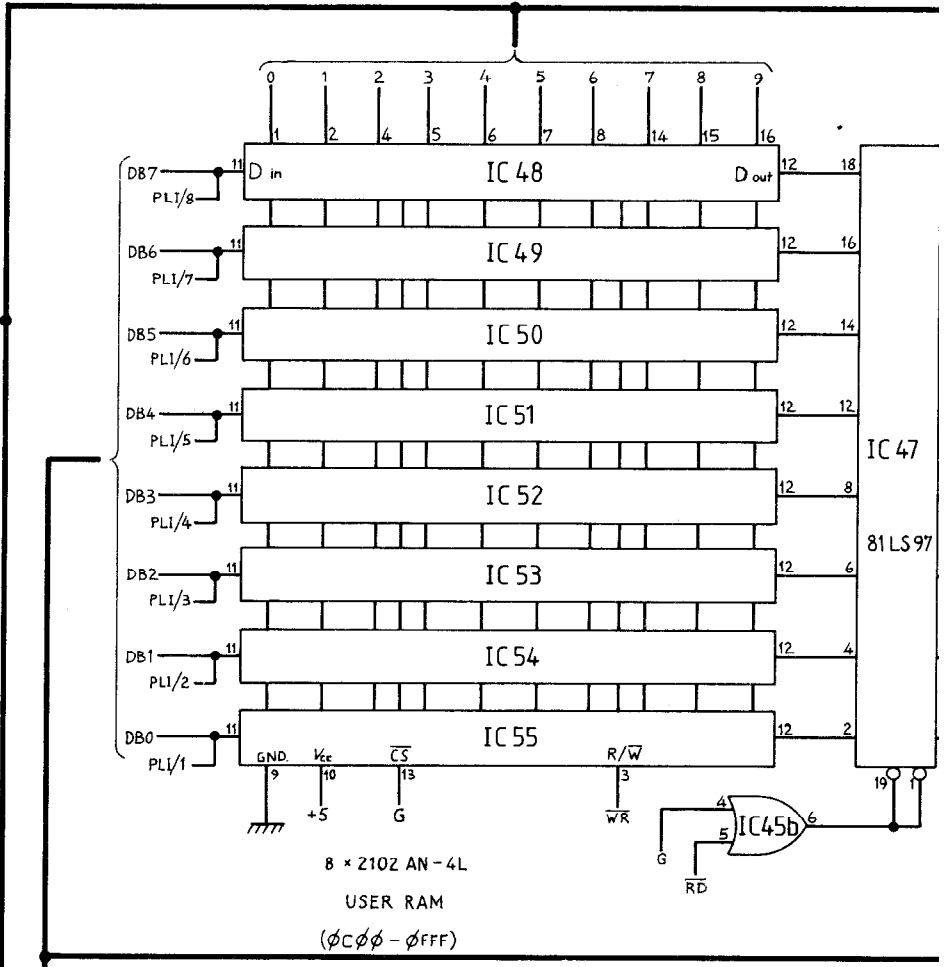
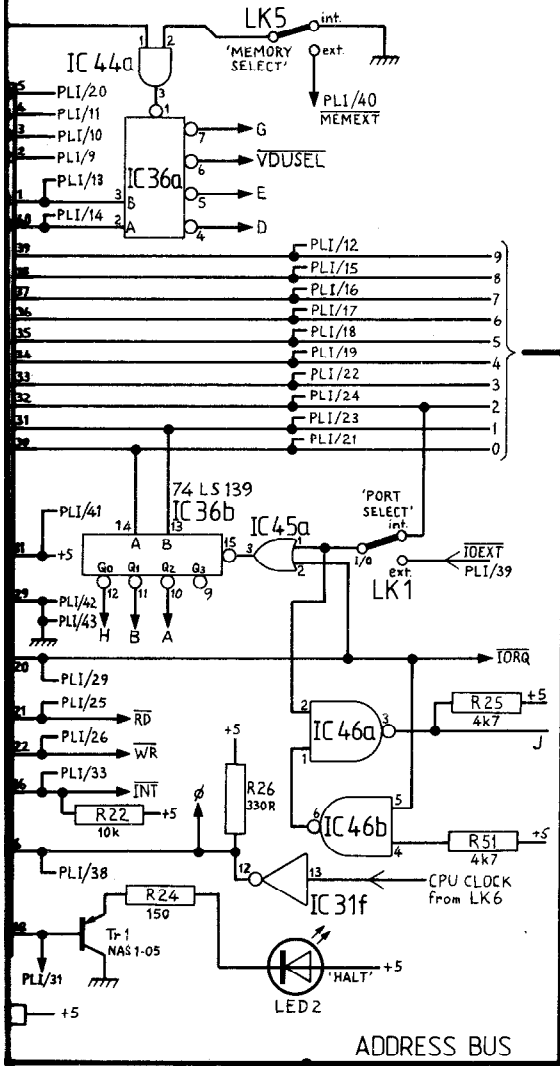
**KEYBOARD CONNECTOR SK1**



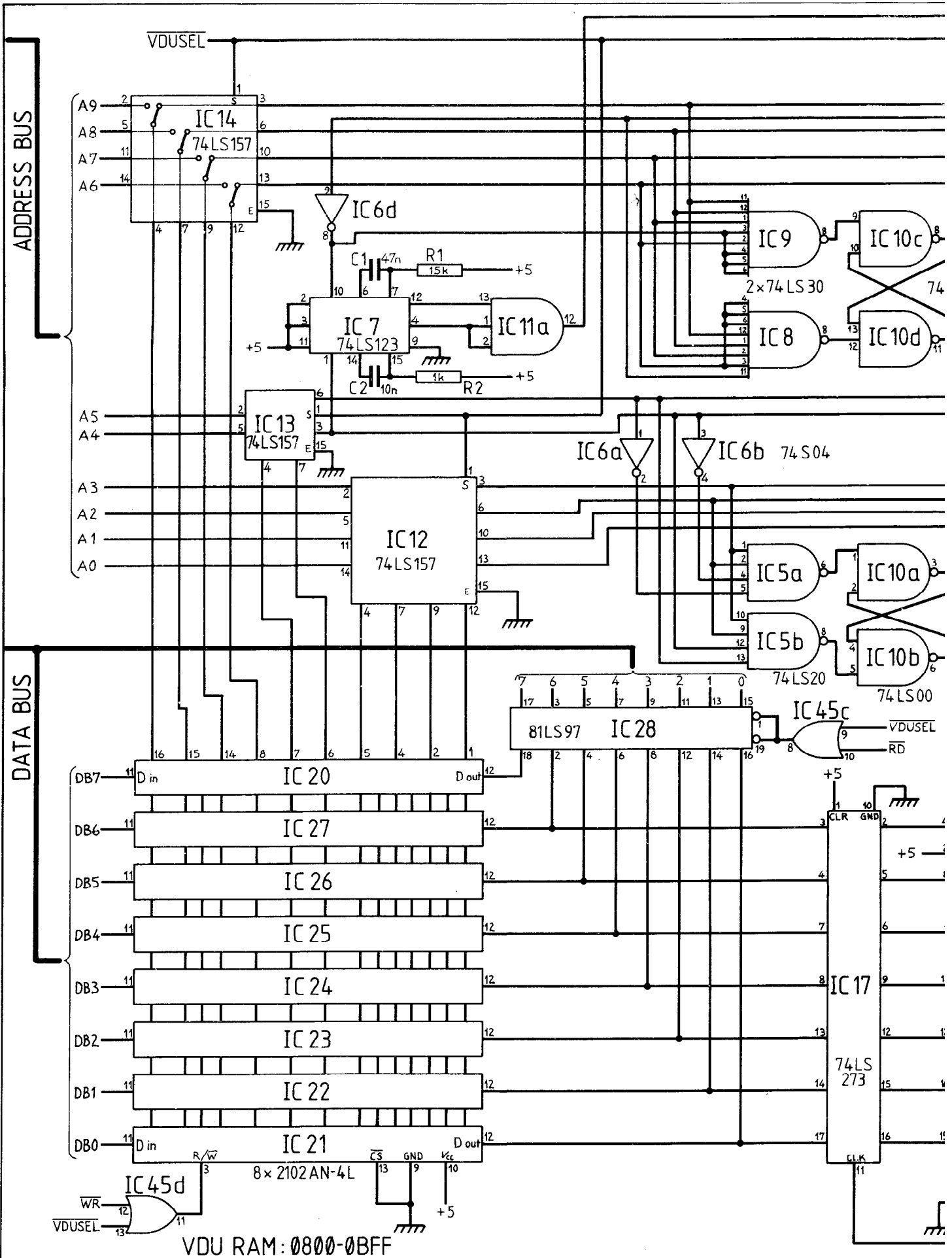
Supersedes Drg. No.  
Copyright Note

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**NASCO SALES Ltd. Chesham, Bucks, England**

**NASCOM 1 - Circu**



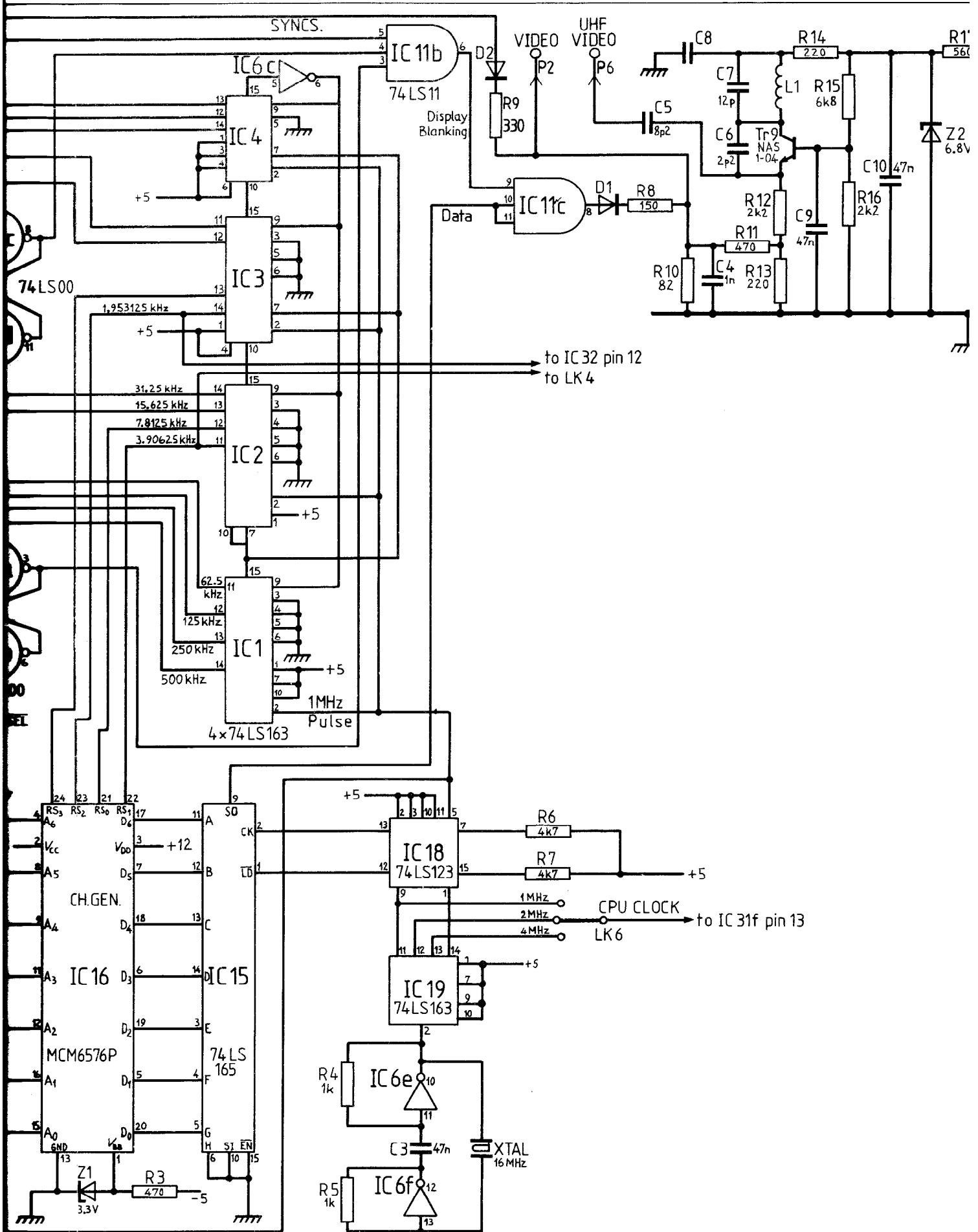
Circuit Diagram



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NASCOM 1 : Circuit

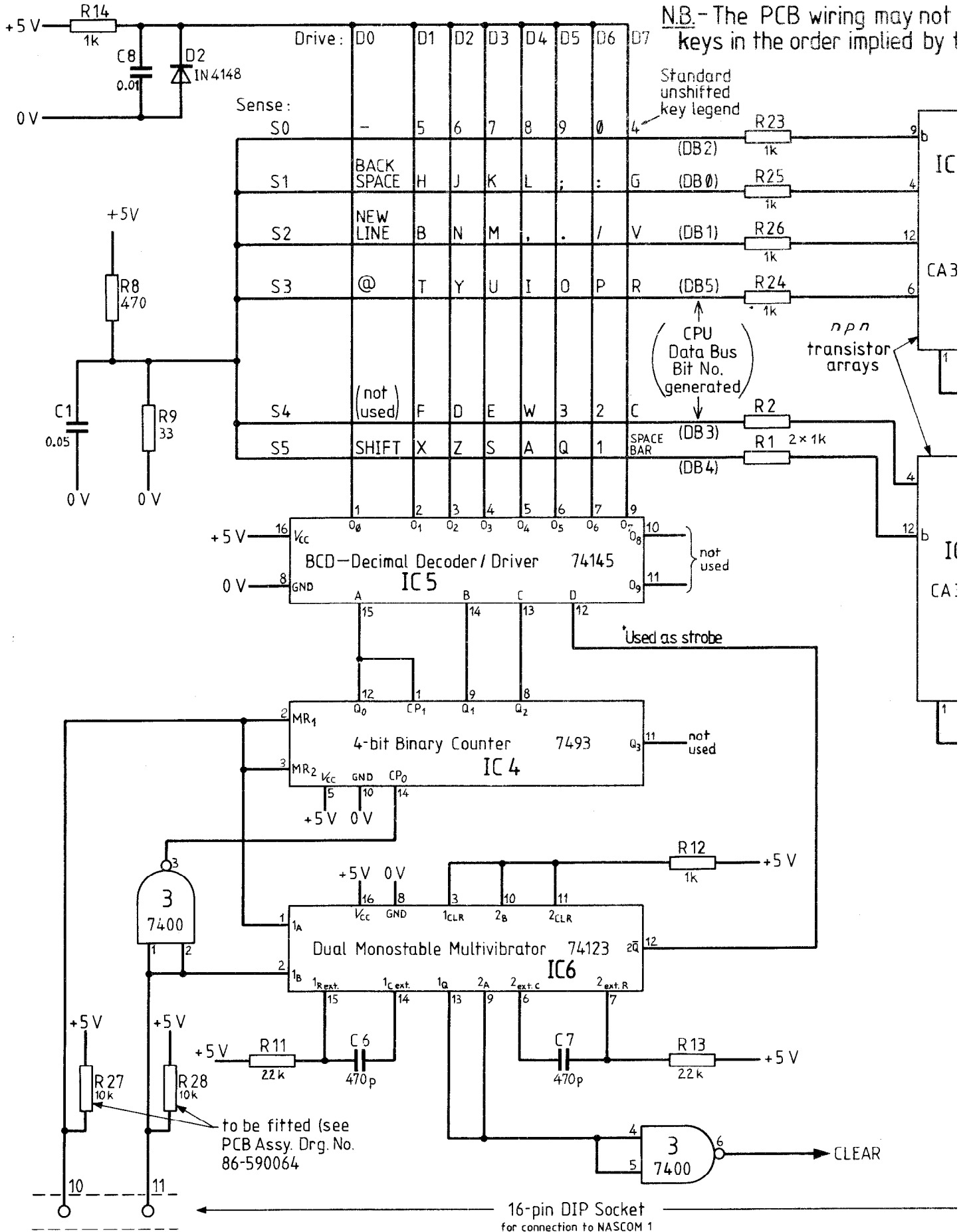


N.B. - The PCB wiring may not be in the order implied by this diagram

Standard unshifted key legend

CPU Data Bus Bit No. generated

n p n transistor arrays

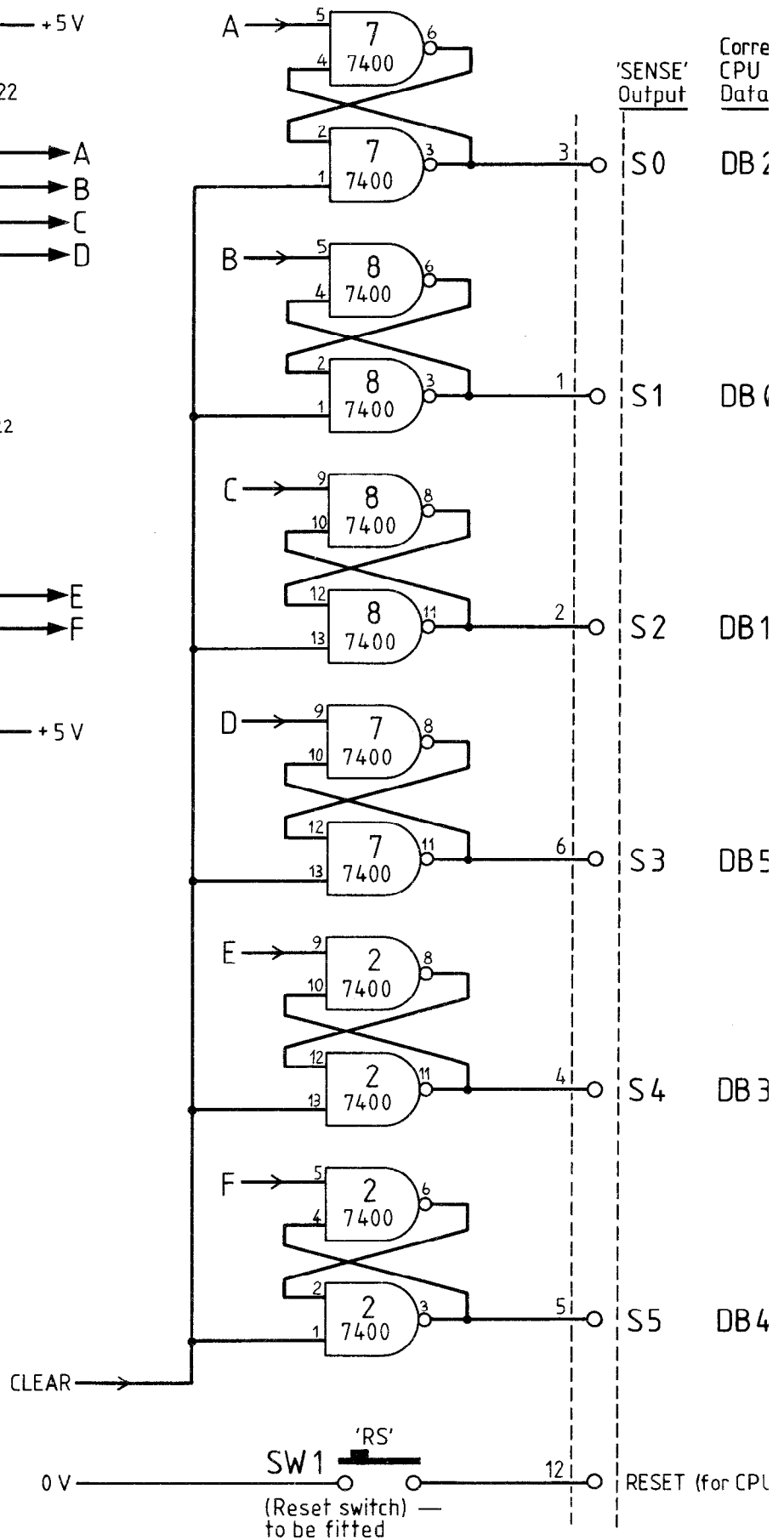
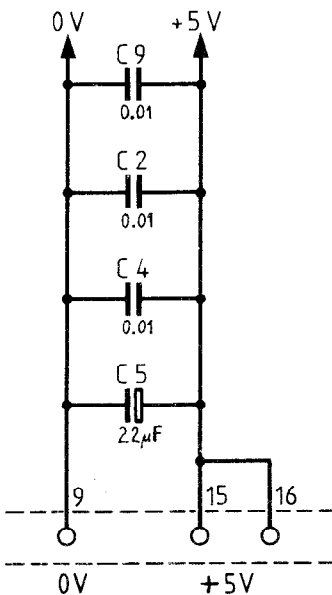
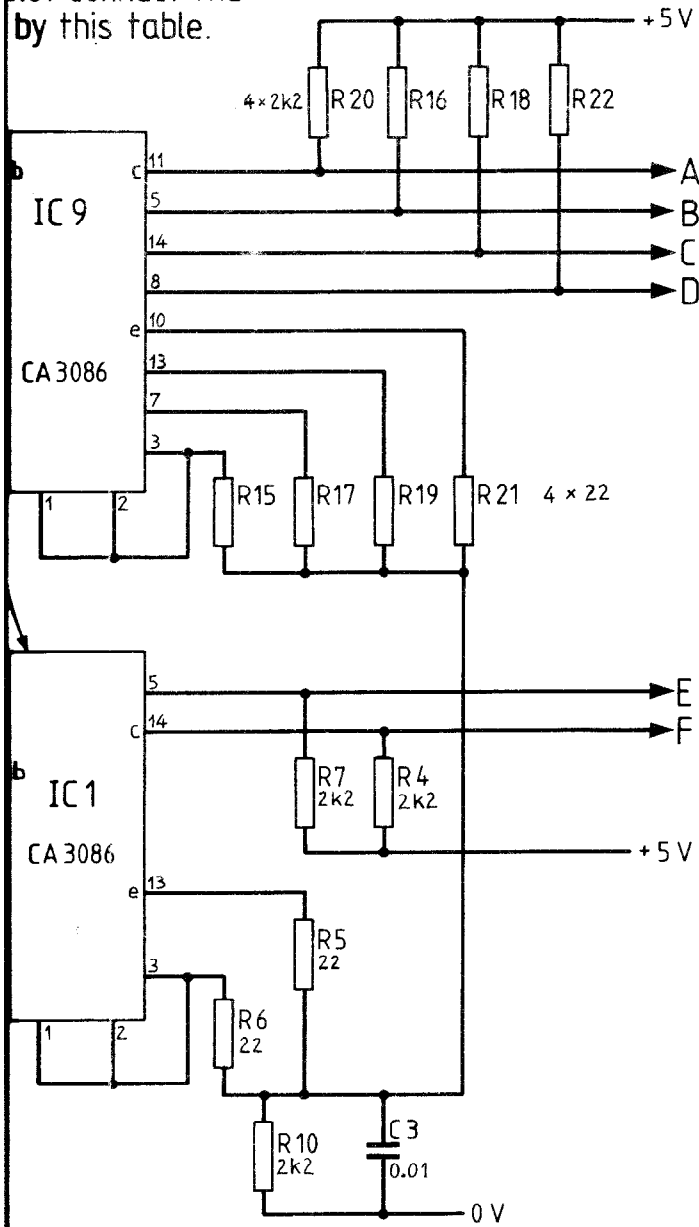


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NASCOM 1 Keyboard  
ref. 55-509032

not connect the  
by this table.

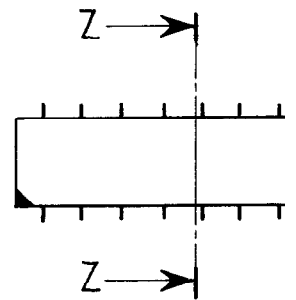
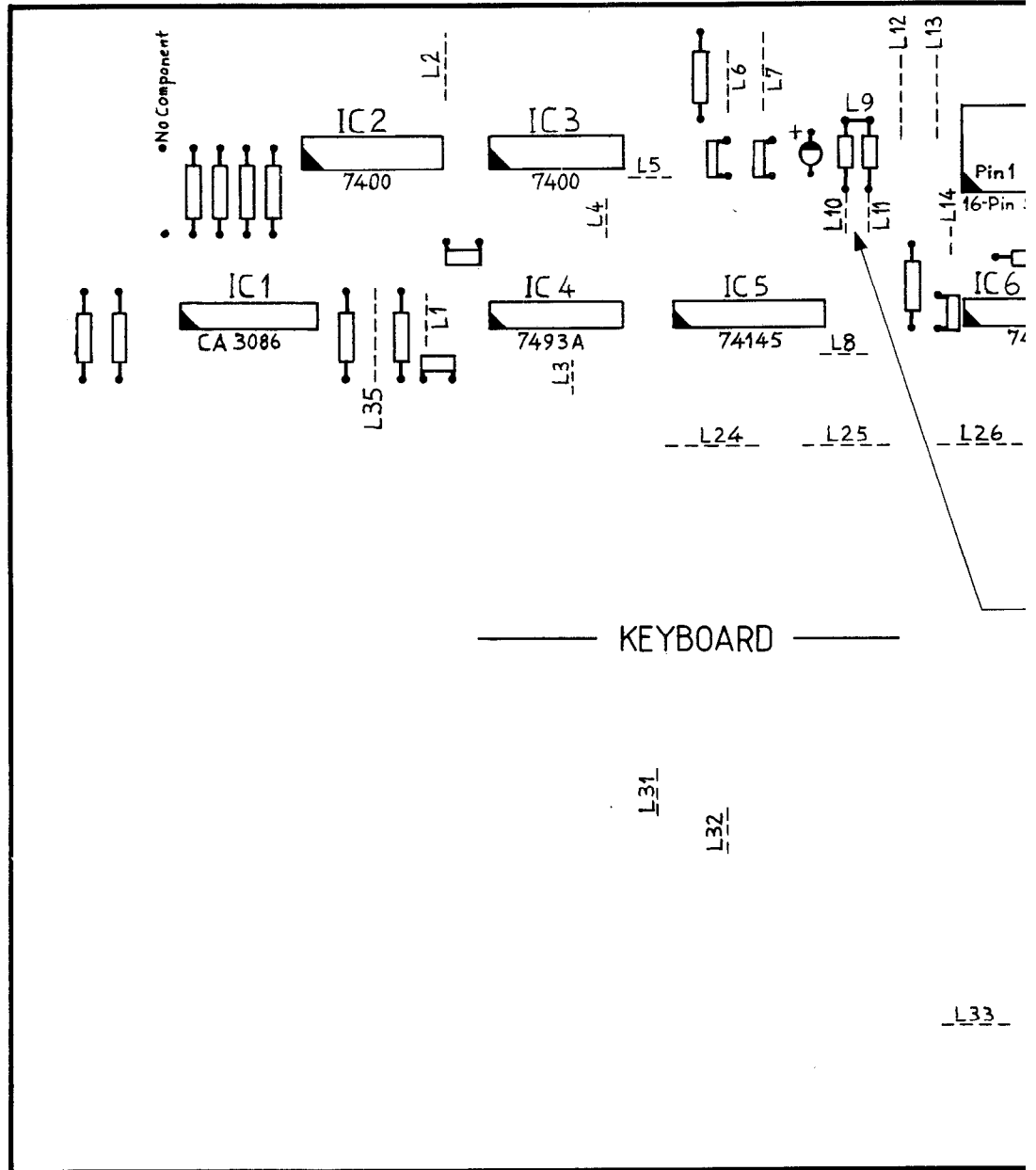


board Circuit Diagram

Note:  
Spare:  $\frac{1}{2}$  IC 1,  $\frac{1}{2}$  IC 3  
Socket pins 7, 8, 13 & 14  
Not allocated: D1 & R3

Issue No. 3    Date Apr 78    Scal  
Sheet No. 1 of 1 sheets  
Drg. No.

CK 9009 (Redrav



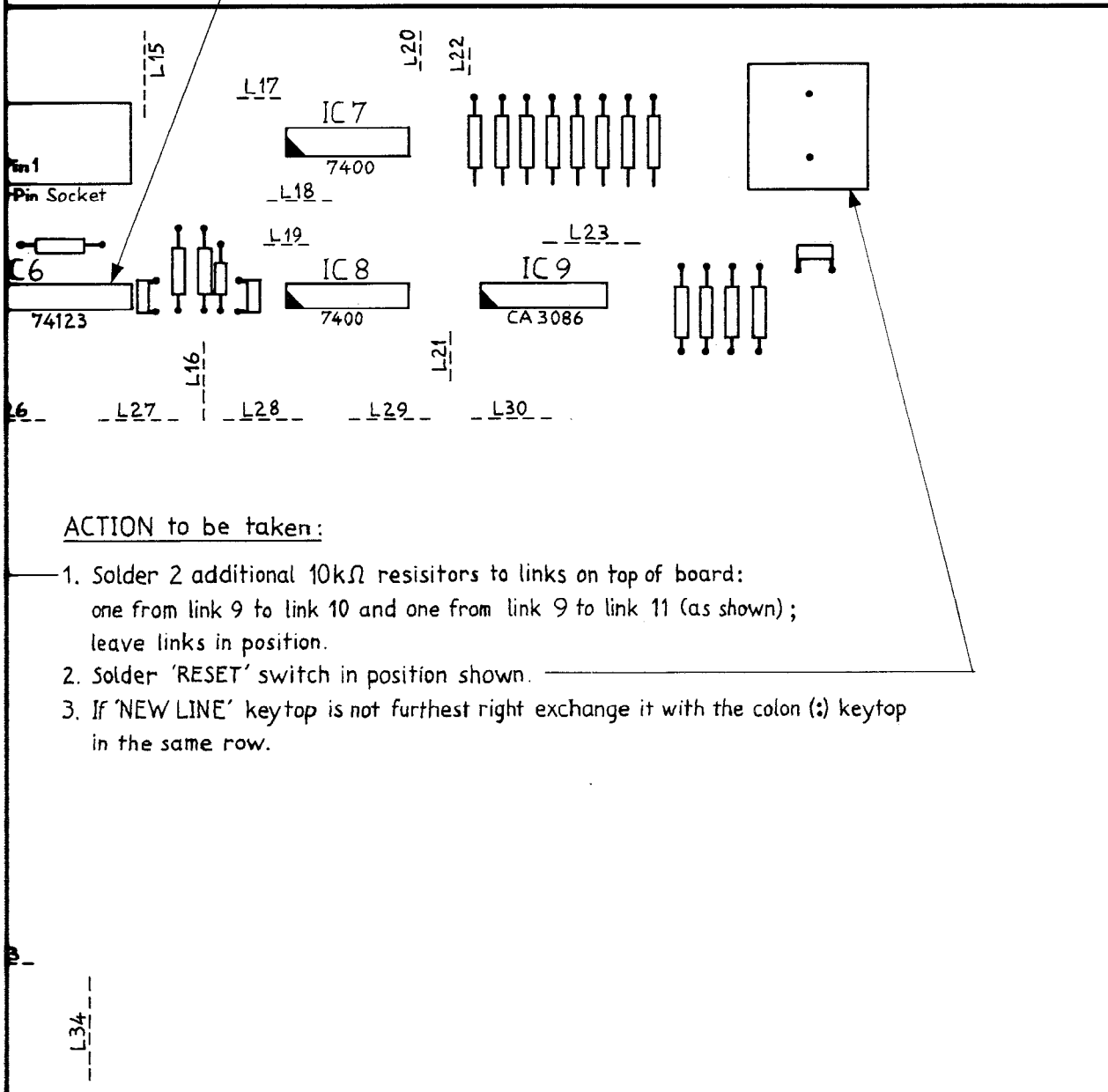
DETAIL 'A' SCAL

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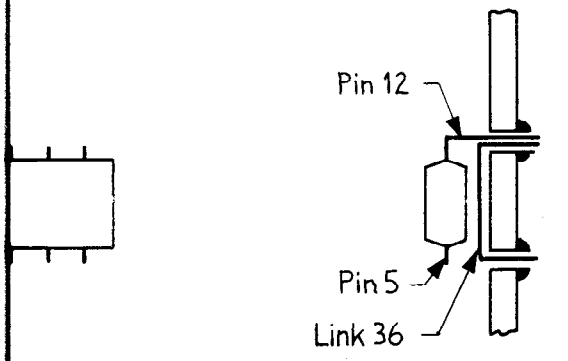
NASCOM 1 KEYBOA  
P.C.B. ASSEMBLY r

See Detail 'A' and Note 1



ACTION to be taken:

1. Solder 2 additional 10kΩ resistors to links on top of board: one from link 9 to link 10 and one from link 9 to link 11 (as shown); leave links in position.
2. Solder 'RESET' switch in position shown.
3. If 'NEW LINE' keytop is not furthest right exchange it with the colon (: ) keytop in the same row.



SCALE 2:1

Section on Z-Z